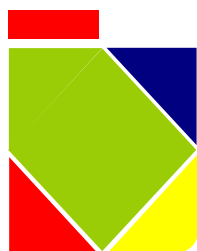


仙公100 *Sancode 100*

The best solution of the low cost and high performance SOC tester



100MHz/256pin Test Header



Signal System Engineering Co., LTD.

- 256/512 IO Channels with PPMU
- 100MHz test rate
- 32/64 DPS with gang function (future)
- 128/256M Pattern depth
- Per pin timing/format generator
- Timing/format change on the fly
- Multi-edge strobe
- Per pin reference level
- Max. 166MHz clock rate
- Tester per board architecture
- Capable of multi-time domain patterns
- 2.3Gbs full-duplex internal tester control bus
- Multi-site test capability
- Audio AWG/Digitizer option

The Sancode 100 test system is a cost competitiveness test platform. It aims at the demand in today's competitive electronics market. The max. 64 site test capability of Sancode 100 increases the throughput and lowers the cost of the production as well. The technology of tester per board architecture which integrated 8 DPS, 4 PMU and 64 PE (with per pin PMU) channels to a single board. Tester per board architecture reduces the complexity of Sancode 100 and enables Sancode 100 very easy to maintain.

With SSE's revolutionary technology, The test pattern depth of Sancode 100 can up to 512M. 512M pattern depth is not only setting a new mark for tester industries but also increasing the capabilities of Sancode 100. 512M pattern depth makes Sancode 100 has the superiors to take care the scan test and embedded memories easily. Compare to the other competitor's testers, they usually need two specific hardware to do scan test and embedded memories separately if testers have not enough pattern memories. In order to download such huge patterns to tester, SSE also built a 2.3Gbs full-duplex tester control bus, it can download 256 channels 16M pattern in 100 seconds only. 5 times faster at least compared with competitor's testers. Sancode 100 can take the advantage of tester per board architecture to generate different time domain patterns. Each digital test system board can generate an independent time domain pattern. For example, Sancode 100 can equip 8 digital system board maximum., so it can generate 8 independent time domain patterns maximum.

In accordance with the spirits of SSE, the user interface of Sancode 100 is user friendly and powerful. User can edit the system driver functions through the GUI and run immediately. Test program compilation is rare during the debug stage. It reduces the burden of test engineers significantly.

Digital Test System Board

-Pin Electronic

Number of IO Channels: 64
Min Period: 10ns
Max Period: 512ns
Period Res: 2ns
Timing Generators: 6 edges
16 sets per pin
Edge Placement Res: 50ps
Edge to Edge Placement Res: 2ns
Pattern Depth: 128M/256M/512M
Driver Range: -2V to +7V
Comparator Range: -2V to +7V
PPMU Current Range: -32mA to 32mA
PPMU Voltage Range: -2V to +7V

-DPS

Number of resources: 8
Voltage Range: -10V to +14V
Current Range: -1A to +1A

-PMU

Number of resource: 4
Voltage Range: -10V to +14V
Current Range: -200mA to +200mA

Analog Board

-AWG

Number of resource: 8
Output Voltage Range: -2.5V to +2.5V
Sample Rate: 2Msps
Resolution: 16 bits
Pattern Depth: 8M

-Digitizer

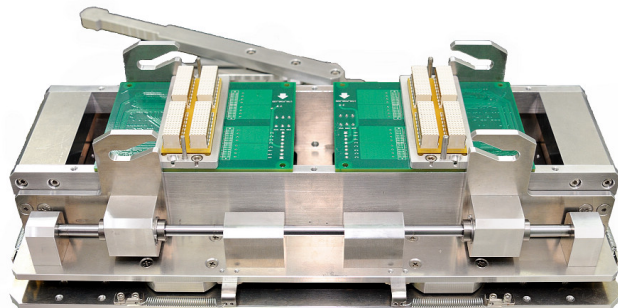
Number of resource: 8
Input Voltage Range: -25V to +25V
Sample Rate: 1Msps
Resolution: 18 bits
Pattern Depth: 8M



Test System Host

- CPU: Intel Pentium Dual-Core or compatible
- Main Memory: 1GB
- Disk Storage: 250GB
- Display: 17" color
- Prober/Handler Interface: GPIB, TTL
- Networking: Ethernet
- Operation System: Windows XP SP2
- Tester Software: TADS

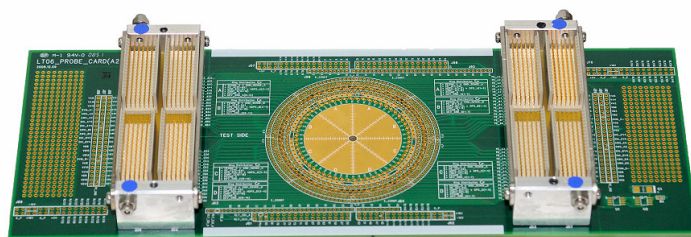
Fixture



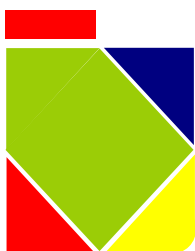
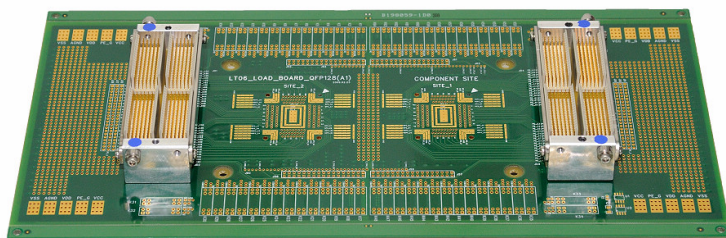
Facilities Requirements

- AC Power: 220V, 3Ø, 4W, 50/60Hz, 30A
- Operation Temp.: 16°C~27°C
- Humidity: 30%~60% RH
- Tester Dimensions
H : 195CM
W : 144CM
D : 187CM

Probe Card



Load Board



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